# **BLIND SIGNAL DETECTION**

A THESIS

submitted by

# ALUWALA SAI HARSHITA EE17B002

for the award of the degree

of

# AND MASTER OF TECHNOLOGY IN ELECTRICAL ENGINEERING



DEPARTMENT OF ELECTRICAL ENGINEERING INDIAN INSTITUTE OF TECHNOLOGY MADRAS.

MAY 2022

THESIS CERTIFICATE

This is to certify that the thesis titled BLIND SIGNAL DETECTION, submitted

by ALUWALA SAI HARSHITA, to the Indian Institute of Technology, Madras, for

the award of the degree of MASTER OF TECHNOLOGY AND BACHELOR OF

TECHNOLOGY IN ELECTRICAL ENGINEERING, is a bona fide record of the

research work done by her under my supervision. The contents of this thesis, in full or

in parts, have not been submitted to any other Institute or University for the award of

any degree or diploma.

**Prof. Devendra Jalihal** 

Project Guide Professor Dept. of Electrical Engineering IIT Madras, 600 036

Place: Chennai

Date: May, 2022

# **ACKNOWLEDGEMENTS**

I would like to express my deepest gratitude towards my project guide, Prof. Devendra Jalihal for giving me the opportunity to work on this project and for his constant support and guidance through out the course of the project.

I would like to thank my parents for their encouragement and support throughout my life. Finally, I would also like to thank my friends for extending their help and support at all times.

### **ABSTRACT**

KEYWORDS: Blind Signal Detection, Automatic Modulation Classification,

Cyclostationarity, LSTM Networks

This thesis considers the design of some aspects in the blind signal detection chain -

1) Automatic Modulation Classification: Trained and tested an SVM based classifica-

tion model with cumulants of the signal as features to classify modulation, 2) Frequency

offset and timing estimation: Used cyclostationarity to formulate an optimisation prob-

lem and estimated frequency offset and timing information and 3) ViterbiNet: Trained

and tested an LSTM + Gaussian mixture model network for calculating transition met-

rics in Viterbi in presence of uncertainities. Results for each of these are presented in

this thesis.

ii

### **Contact for full report:**

The complete project report is available with Professor Devendra Jalihal, EE Department, IITM.